



Product Change Notice

Date of Publication:	May 14, 2010
PCN No:	PCN-10-037
Change Subject:	Product Migration from MX25L3205D to MX25L3206E
Change Type:	Product Migration
Key Milestones:	Last Order Date: Dec. 31, 2010 Last Ship Date: Mar. 31, 2011

Description of Changes

Macronix introduces the MX25L3206E as a replacement of the MX25L3205D. All customers are requested to start the migration for their existing design to the MX25L3206E as soon as possible.

MX25L3206E supports new features, SFDP mode and Dual Output mode (Single Input / Dual Output), but no longer supports x2 I/O (Dual Input / Dual Output), Continuously Program (CP) mode and ACC function.

For detail of these changes, please refer to Application Notes of MX25L3206E

Impact of Changes and Recommended Actions

MX25L3206E no longer supports x2 I/O (Dual Input / Dual Output) mode, host systems using the x2 I/O (Dual Input/Dual Output) mode of the MX25L3205D have to be migrated to Single Input/Dual Output mode.

The removal of the ACC mode means that high voltage can no longer be applied to the WP# pin. Doing so will damage the MX25L3206E. Make sure that the programmer does not use the ACC feature. This feature needs to be disabled or updated with the latest MX25L3206E programming algorithm if it is used.

Applications using the MX25L3205D need to migrate to the MX25L3206E, and new designs should only use the MX25L3206E. Migration to MX25L3206E will enable Macronix to provide continuous support to our customers.

General Feature Comparison:

Specification		MX25L3205D	MX25L3206E
Voltage		2.7V ~3.6V	2.7V ~3.6V
Interface		x1, x2	x1, 1I / 2O*
Package		16-SOP (300mil) 8-SOP (209mil) 8-PDIP (300mil) 8-WSON (6x5mm) 8-USON (4x4mm)	16-SOP (300mil) 8-SOP (209mil) 8-PDIP (300mil) 8-WSON (6x5mm) 8-USON (4x4mm)
Operating Temperature		-40°C~85°C	-40°C ~85°C
Sector Structure		4KB / 64KB	4KB / 64KB
Clock Rate	Fast Read	10KHz~86MHz	DC~86MHz
	Read	10KHz~33MHz	DC~33MHz
	2 Read (x2 I/O)	10KHz~50MHz	--
	DREAD (Dual Output)	--	DC~80MHz
Byte Program		Yes	Yes
SFDP Mode		No	Yes
CP Mode (Continuously Program)		Yes	No
ACC Mode		Yes	No
Data Protection	Security OTP	512 bits	512 bits
	Block Protection	BP3~BP0	BP3~BP0
Hold#		Yes	Yes
WP#		Yes	Yes
Endurance (typ.)		100K	100K
Data Retention		20y	20y

Note:

* Dual Output mode (1I/2O) means Single Input / Dual Output.

Products Affected:

Part Number	Package		Part Number	Package
MX25L3205DM2I-12G	209MIL 8-SOP	→	MX25L3206EM2I-12G	209MIL 8-SOP
MX25L3205DMI-12G	300MIL 16-SOP	→	MX25L3206EMI-12G	300MIL 16-SOP
MX25L3205DPI-12G	300MIL 8-PDIP	→	MX25L3206EPI-12G	300MIL 8-PDIP
MX25L3205DZNI-12G	6x5 8-WSON	→	MX25L3206EZNI-12G	6x5 8-WSON
MX25L3205DZUI-12G	4x4 8-USON	→	MX25L3206EZUI-12G	4x4 8-USON

Production Status for E version:

▶ Sample Availability	Apr/2010
▶ Mass Production	May/2010

For the contact and order information, please visit Macronix's Web site at:

<http://www.macronix.com>